



डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग
DEPARTMENT OF ELECTRONICS ENGINEERING
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

क्रमांक: DECE/ISRO/1624 / 2025-26

दिनांक: 14/11/2025

Recruitment of Project Staff
Junior Research Fellow / Project Associate
 on Purely Contract Basis

Sponsored Research Project: Onboard spectral preprocessing for multispectral image compression using FPGA

List of Short Listed Candidates:

Sr. No.	Name of candidates	Eligible for RA	Remarks
1.	Saurav Kumar Pandey	Eligible	--
2.	Piyush Tinabhai Vaniya	Eligible	--
3.	Sushma Singh	Provisional Eligible	Not applied in Application Form, No Mark sheet & Certificates are attached

- Interview for the shortlisted candidates will be held on **20th November, 2025** from 11:30 am in Department of Electronics Engineering (New Building). Short listed candidates must report by 10:00 am in Department office. Candidate, who can't attend interview physically, can send request for Online Interview process through e-mail id: add@eced.svnit.ac.in with valid reasons.
- Candidate must bring relevant original certificate, experience letter (if any) and 2 photographs at the time of interview.
- List of selected candidates will be displayed on the institute website within one week after interview held.
- No TA/DA will be paid for attending the Interview.


 Dr. A. D. Darji
 PI, ISRO Project